# Switching (-20V, -2.0A)

# RTR020P02

#### Features

- 1) Low On-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small and Surface Mount Package (TSMT3).

# Application

Power switching, DC / DC converter.

#### ●Structure

Silicon P-channel MOS FET

# Packaging specifications

	Package	Taping
Type	Code	TL
	Basic ordering unit (pieces)	3000
RTR020P02		0

# ● Absolute maximum ratings (Ta=25°C)

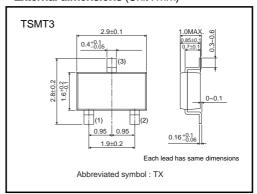
Parameter		Symbol	Limits	Unit	
Drain-source voltage		V <sub>DSS</sub>	-20	V	
Gate-source voltage		V <sub>GSS</sub>	±12	V	
Drain current	Continuous	I <sub>D</sub>	±2.0	Α	
	Pulsed	I <sub>DP</sub> *1	±8.0	А	
Source current	Continuous	Is	-0.8	Α	
(Body diode)	Pulsed	I <sub>SP</sub> *1	-3.2	Α	
Total power dissipation		P <sub>D</sub> *2	1.0	W	
Channel temperature		Tch	150	°C	
Range of Storage temperature		Tstg	-55 to +150	°C	

- \*1 Pw≤10µs, Duty cycle≤1%
- \*2 Mounted on a ceramic board

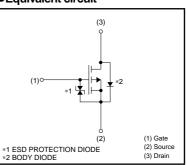
# ●Thermal resistance (Ta=25°C)

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth (ch-A)	125	°C / W

# ●External dimensions (Unit:mm)



# ●Equivalent circuit



# ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	_	±10	μΑ	Vgs=±12V, Vps=0V
Drain-source breakdown voltage	V <sub>(BR) DSS</sub>	-20	_	_	V	$I_D = -1 \text{mA}, V_{GS} = 0 \text{V}$
Zero gate voltage drain current	IDSS	_	_	-1	μΑ	V <sub>DS</sub> = -20V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS (th)</sub>	-0.7	_	-2.0	٧	V <sub>DS</sub> = -10V, I <sub>D</sub> = -1mA
Static drain-source on-state resistance	R <sub>DS (on)</sub> *	-	100	135	mΩ	I <sub>D</sub> = -2.0A, V <sub>G</sub> S= -4.5V
		-	110	150	mΩ	I <sub>D</sub> = -2.0A, V <sub>G</sub> S= -4.0V
		-	180	250	mΩ	I <sub>D</sub> = -1.0A, V <sub>G</sub> S= -2.5V
Forward transfer admittance	Y <sub>fs</sub> *	1.2	-	_	S	V <sub>DS</sub> = -10V, I <sub>D</sub> = -1.0A
Input capacitance	Ciss	_	430	_	рF	V <sub>DS</sub> = -10V
Output capacitance	Coss	_	80	_	pF	V <sub>GS</sub> =0V
Reverse transfer capacitance	Crss	_	55	_	pF	f=1MHz
Turn-on delay time	t <sub>d (on)</sub> *	_	11	_	ns	ID= -1.0A
Rise time	tr *	_	13	_	ns	V <sub>DD</sub> ≒ -15V
Turn-off delay time	t <sub>d (off)</sub> *	_	38	_	ns	V <sub>GS</sub> = -4.5V R <sub>L</sub> =15Ω
Fall time	t <sub>f</sub> *	_	12	_	ns	$R_{GS}=10\Omega$
Total gate charge	Qg	_	4.9	_	nC	V <sub>DD</sub> ≒-15V
Gate-source charge	Qgs	_	1.2	-	nC	Vgs=-4.5V
Gate-drain charge	Qgd	ı	1.3	_	nC	I <sub>D</sub> = -2.0A

\*Pulsed

Body diode characteristics (source-drain characteristics)

Forward voltage  $V_{SD}$  - - -1.2 V  $I_{S}$ = -0.8A,  $V_{GS}$ =0V

# Electrical characteristic curves

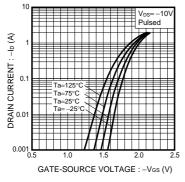


Fig.1 Typical Transfer Characteristics

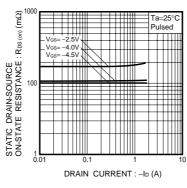


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current

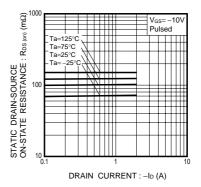


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

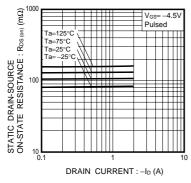


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current

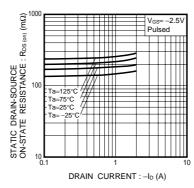


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

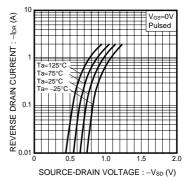


Fig.6 Reverse Drain Current vs.Source-Drain Voltage

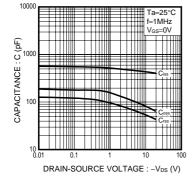


Fig.7 Typical Capacitance vs. Drain-Source Voltage

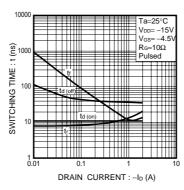


Fig.8 Switching Characteristics

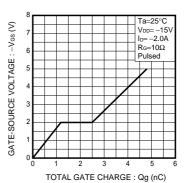


Fig.9 Dynamic Input Characteristics

# ●Measurement circuits

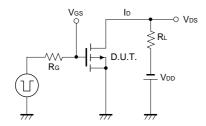


Fig.10 Switching Time Test Circuit

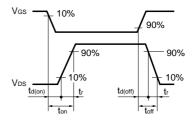


Fig.11 Switching Time Waveforms

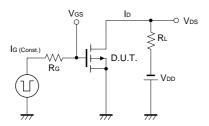


Fig.12 Gate Charge Test Circuit

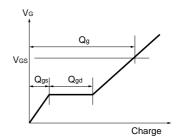


Fig.13 Gate Charge Waveform

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